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APPLICANTS Hiroyasu Jobetto, Tokyo, JAPAN; Ichiro Mihara, Tokyo, JAPAN;				
** CONTINUING DATA ***** This application is a CON of PCT/JP03/09958 08/05/2003 <i>verified</i>				
** FOREIGN APPLICATIONS ***** JAPAN 2002-232289 08/09/2002 JAPAN 2002-232737 08/09/2002 JAPAN 2002-254695 08/30/2002 <i>verified</i>				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 06/25/2004				
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Verified and Acknowledged <i>Allowance</i> Examiner's Signature Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 84	TOTAL CLAIMS 38
INDEPENDENT CLAIMS 5				
ADDRESS 01933				
TITLE Semiconductor device and method of manufacturing the same				
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